

FIG. 1 FIRST PROCESS OF SWITCHING ELECTRODES (L RESET)
Step 1

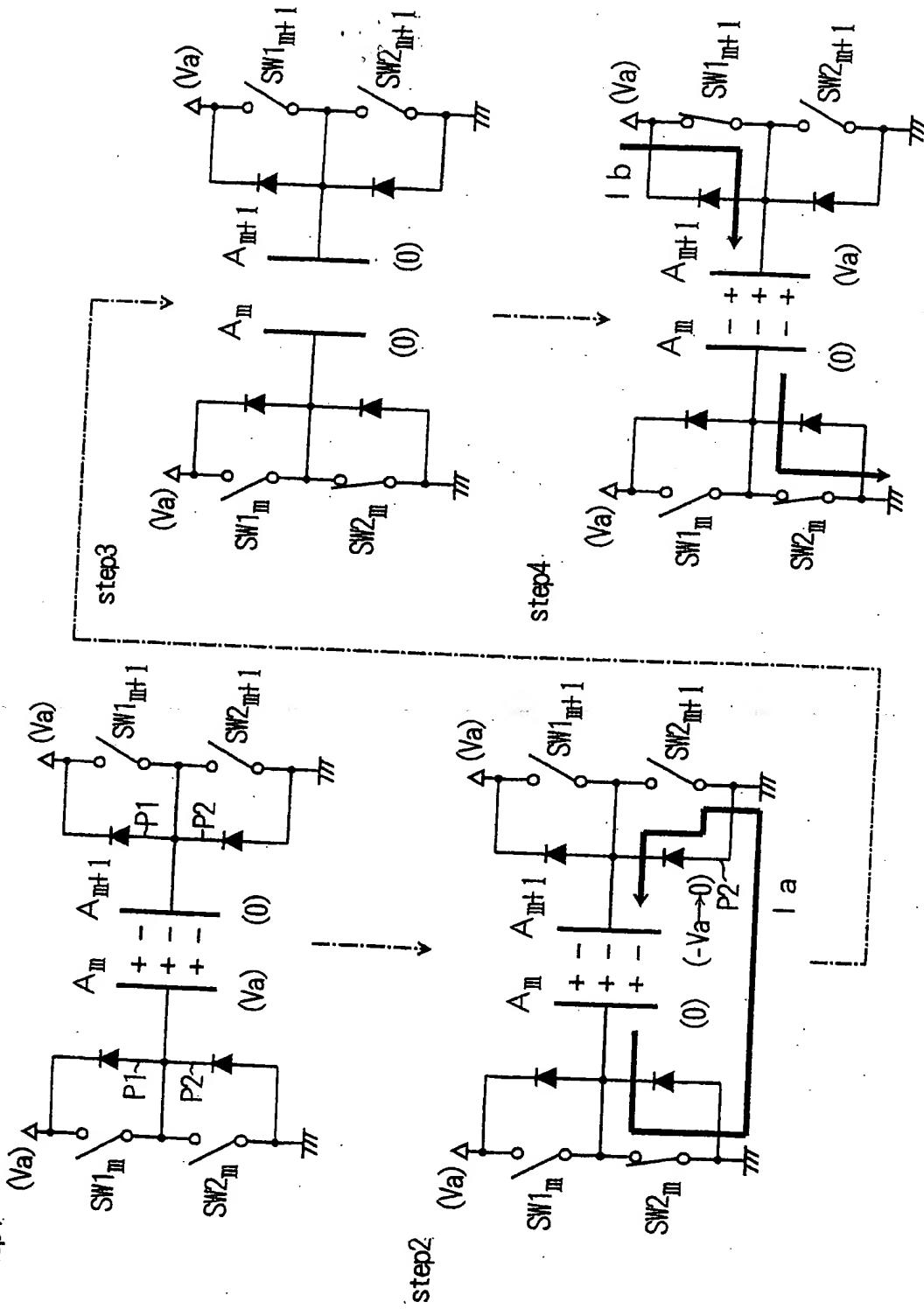


FIG. 2

SECOND PROCESS OF SWITCHING ELECTRODES (H RESET)

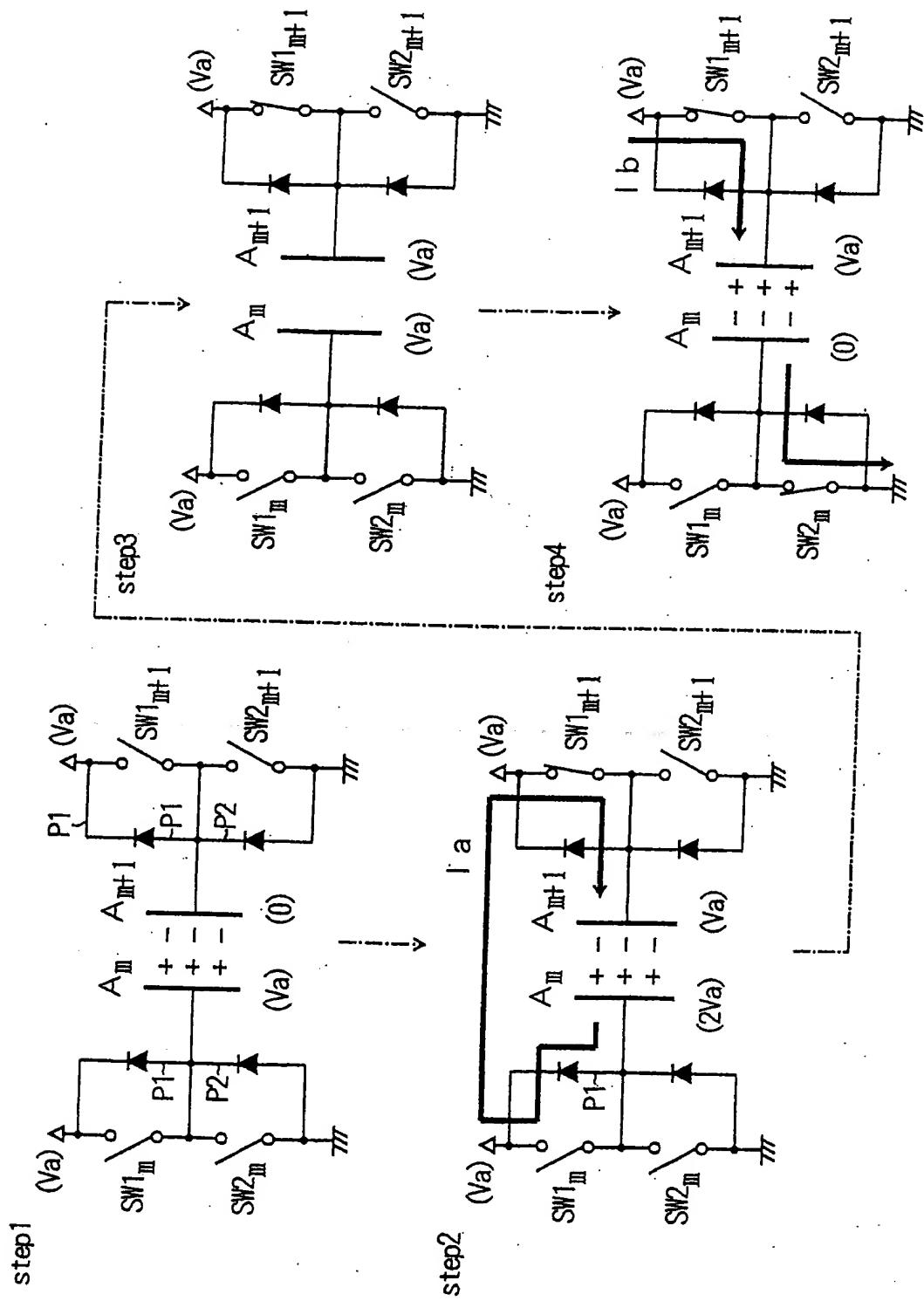


FIG. 3

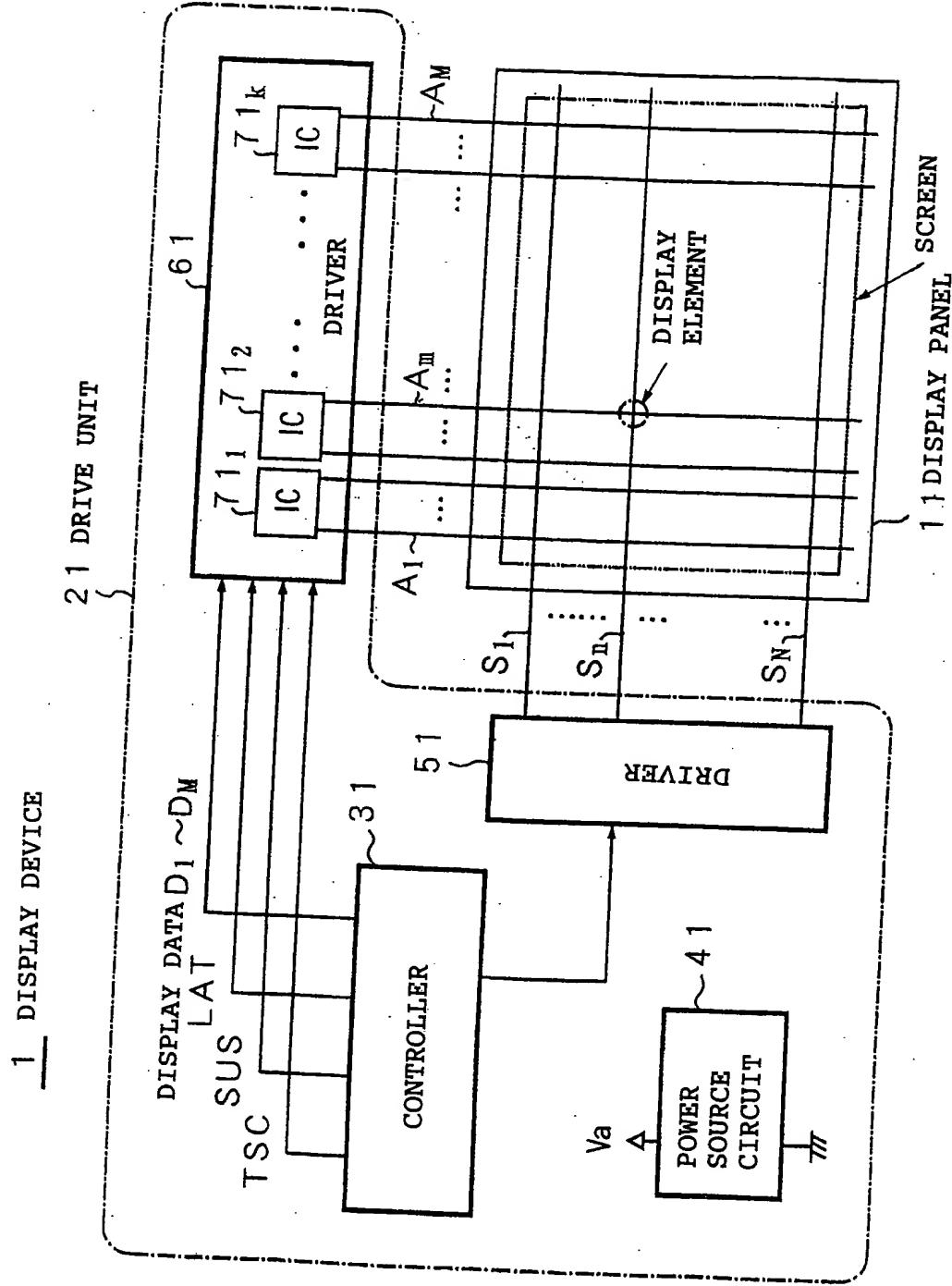


FIG. 4

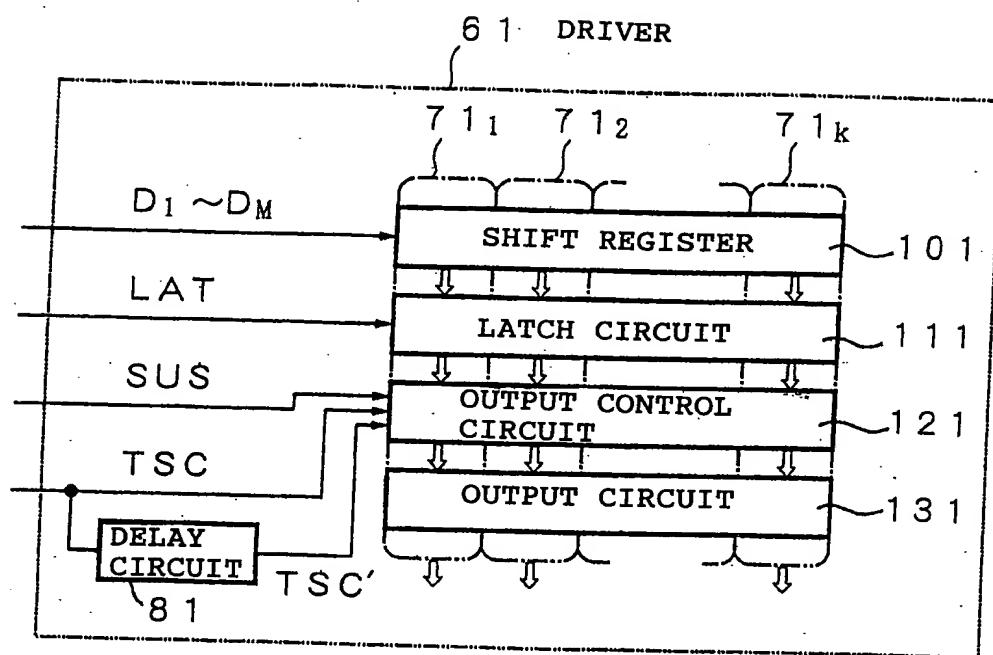


FIG. 5

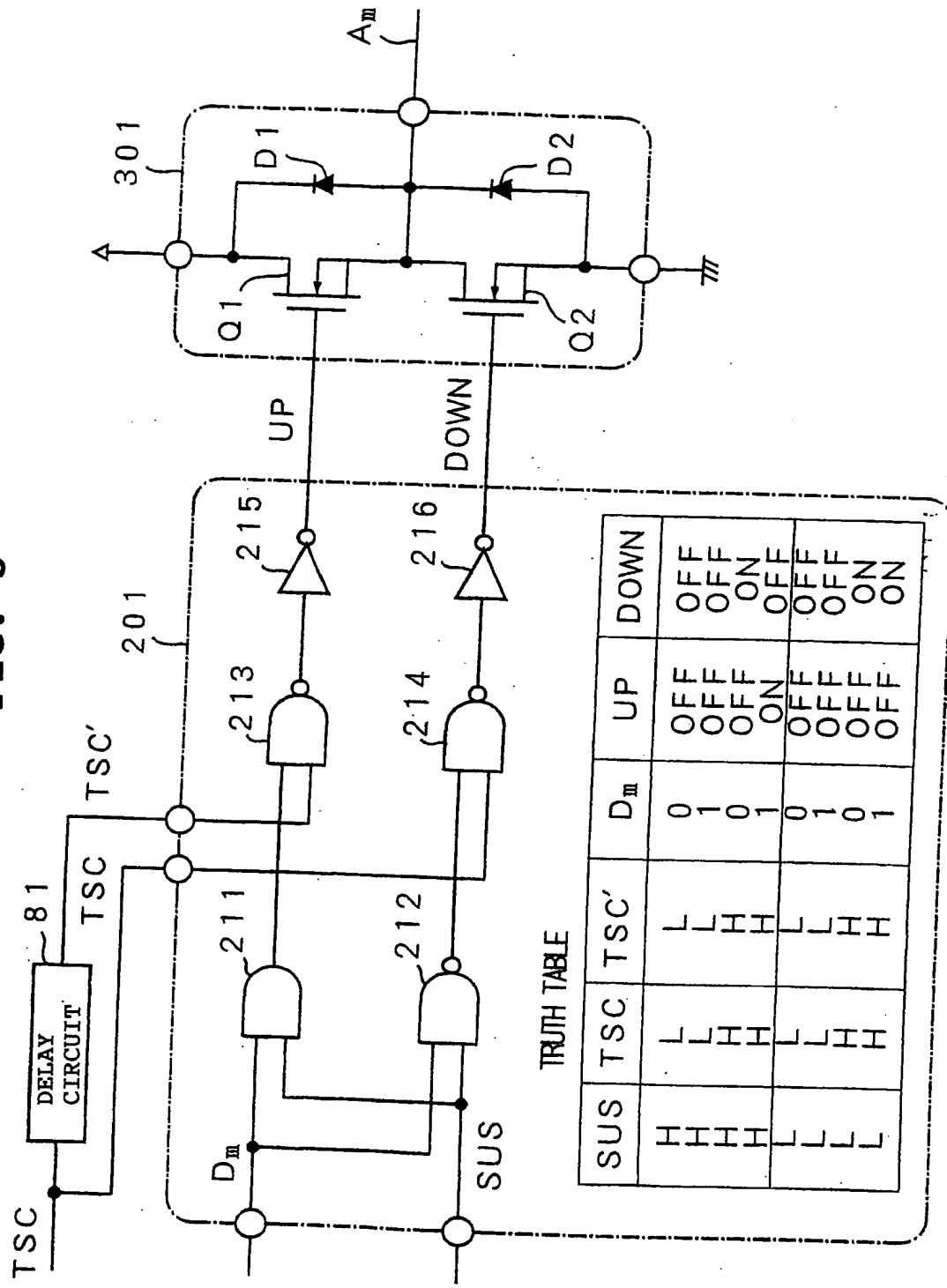
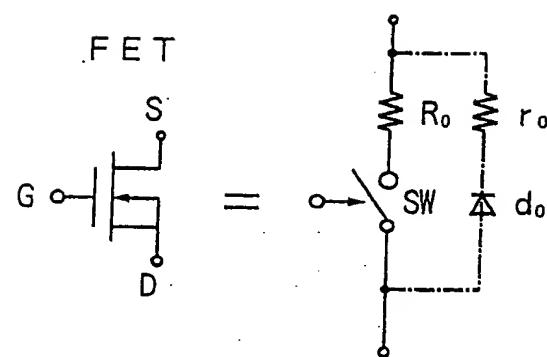


FIG. 6



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FIG. 7

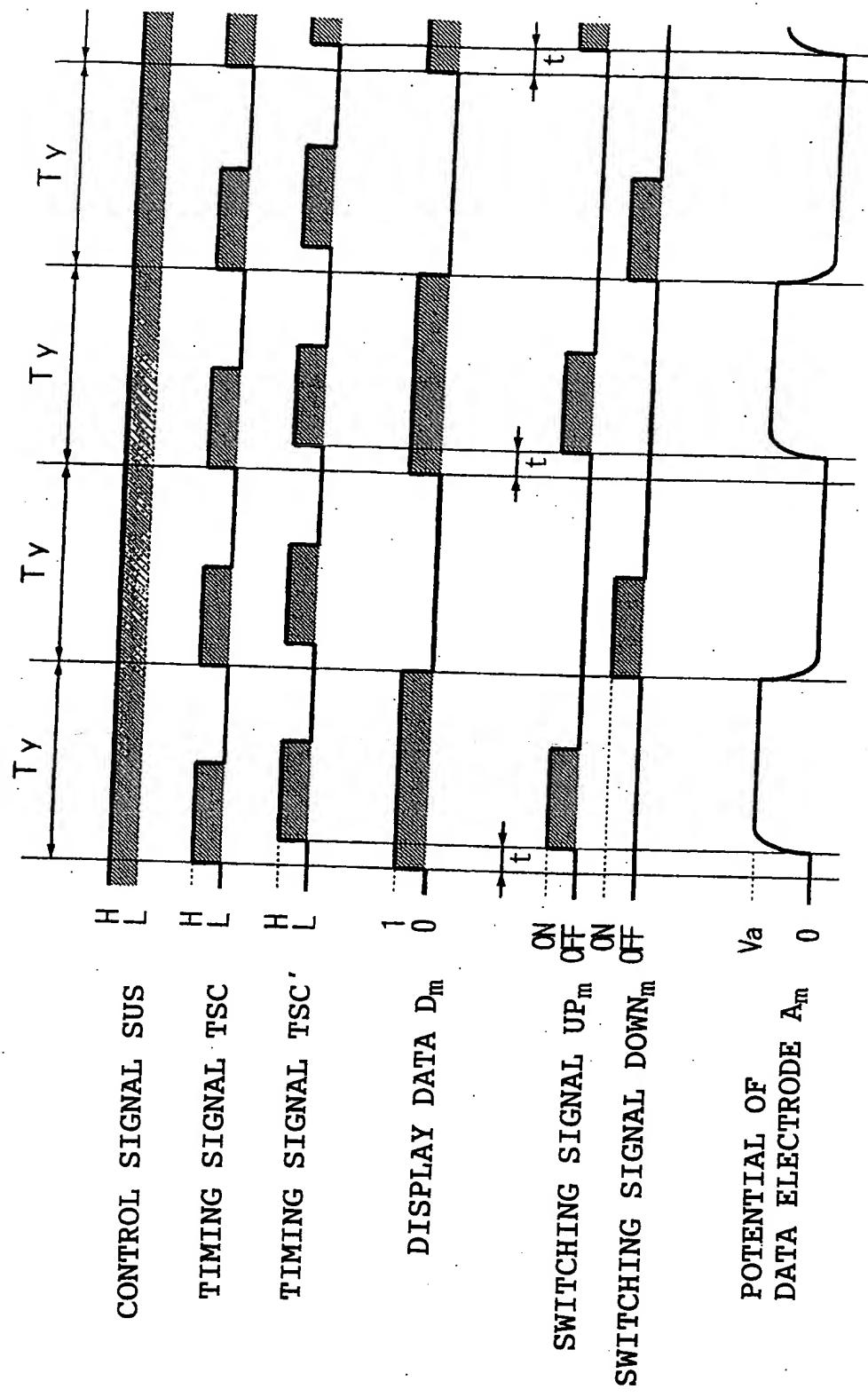


FIG. 8

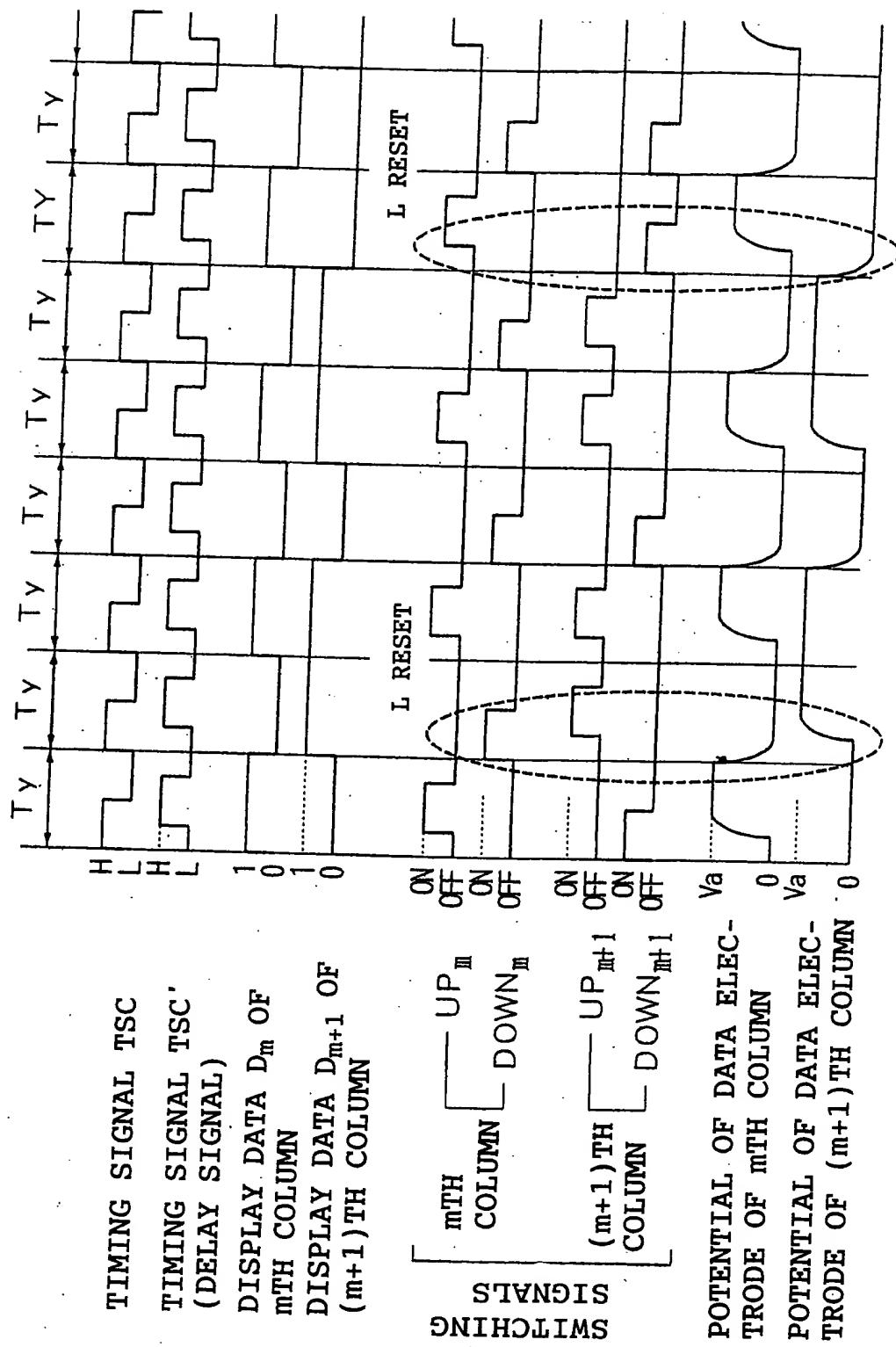


FIG. 9A

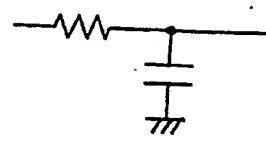


FIG. 9B

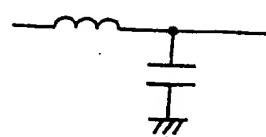
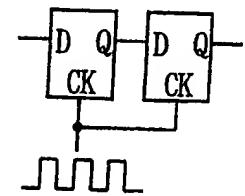


FIG. 9C



FIG. 9D



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FIG. 10

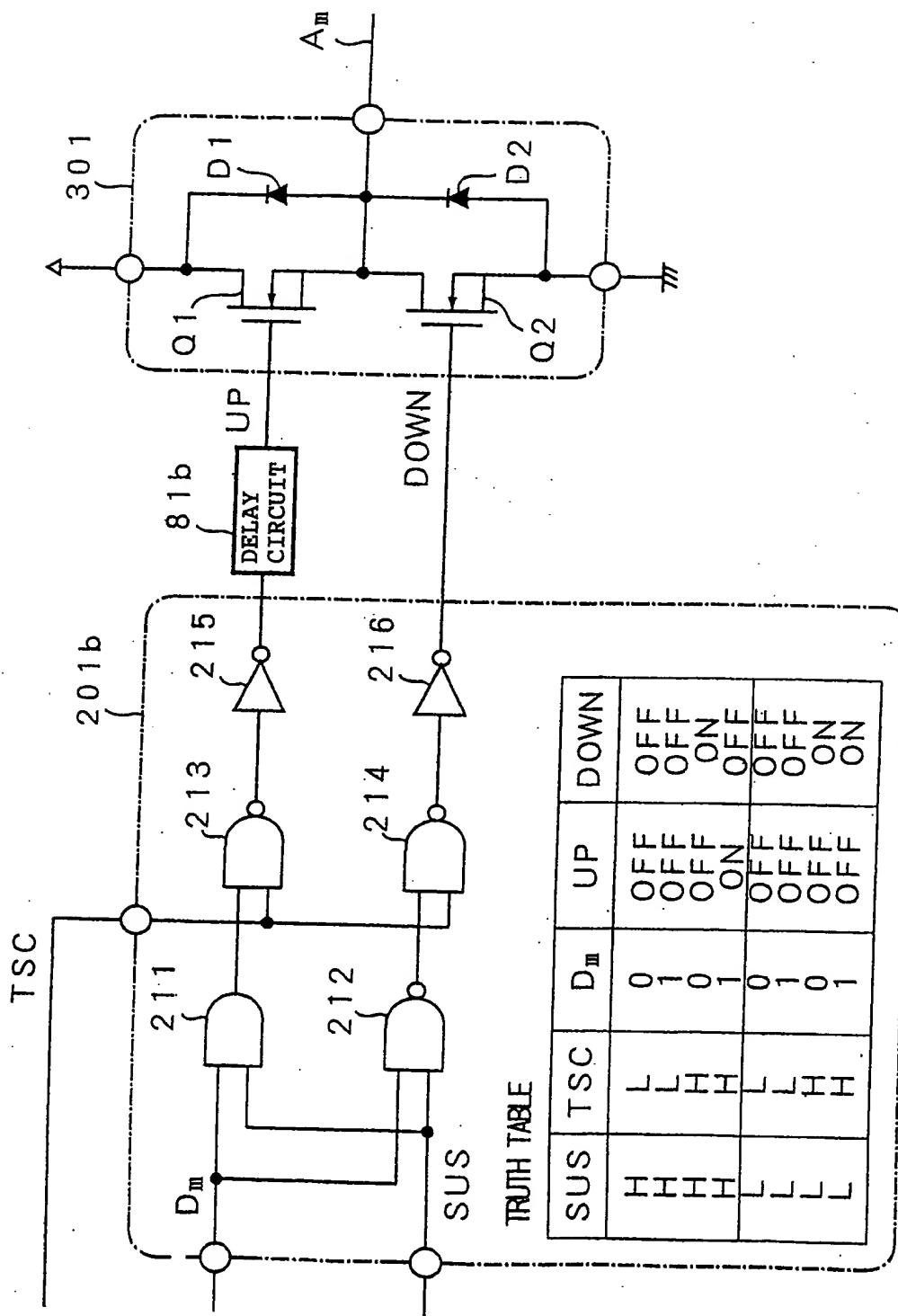
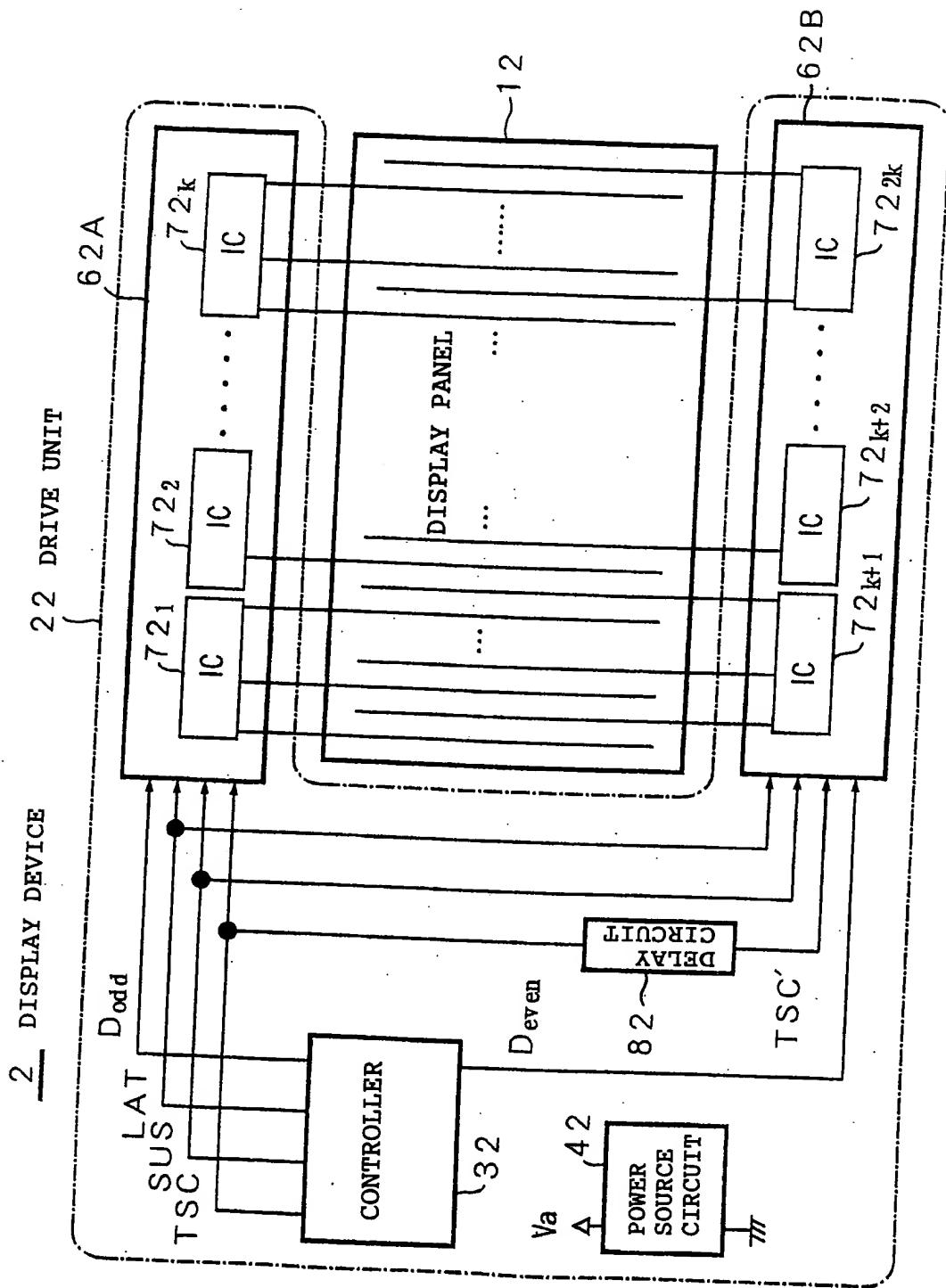


FIG. 11



TO 20900-48252860

FIG. 12

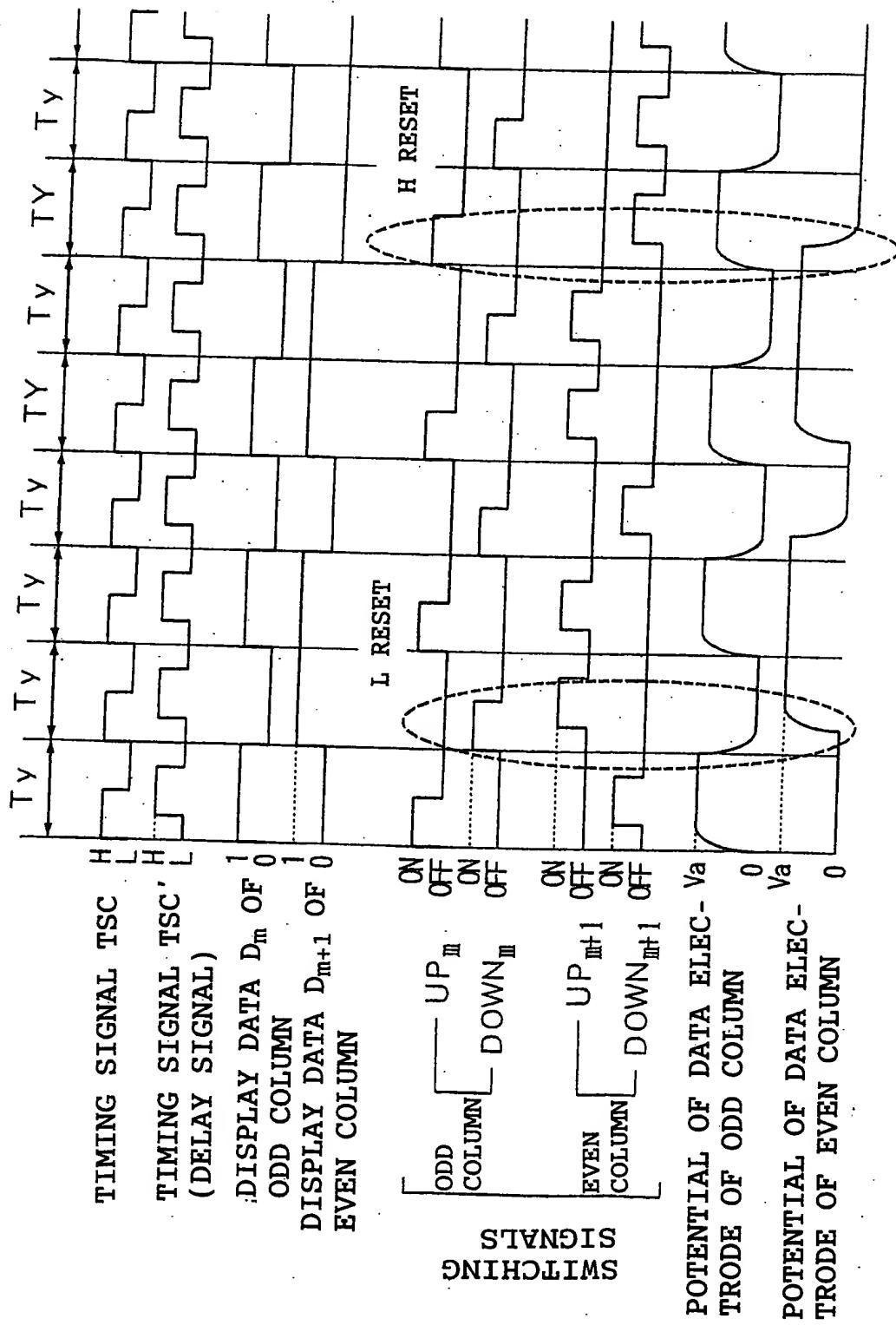


FIG. 13

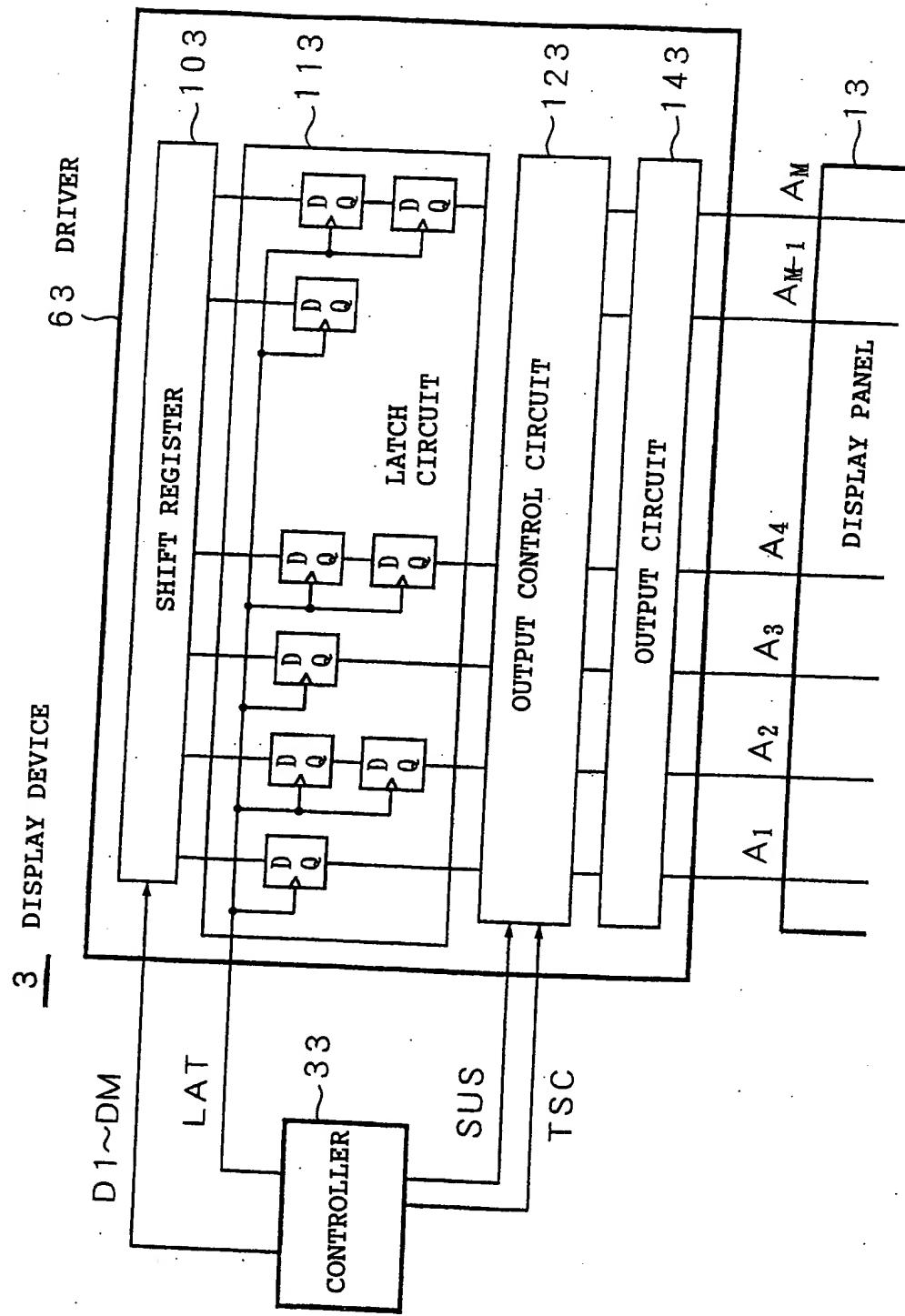


FIG. 14

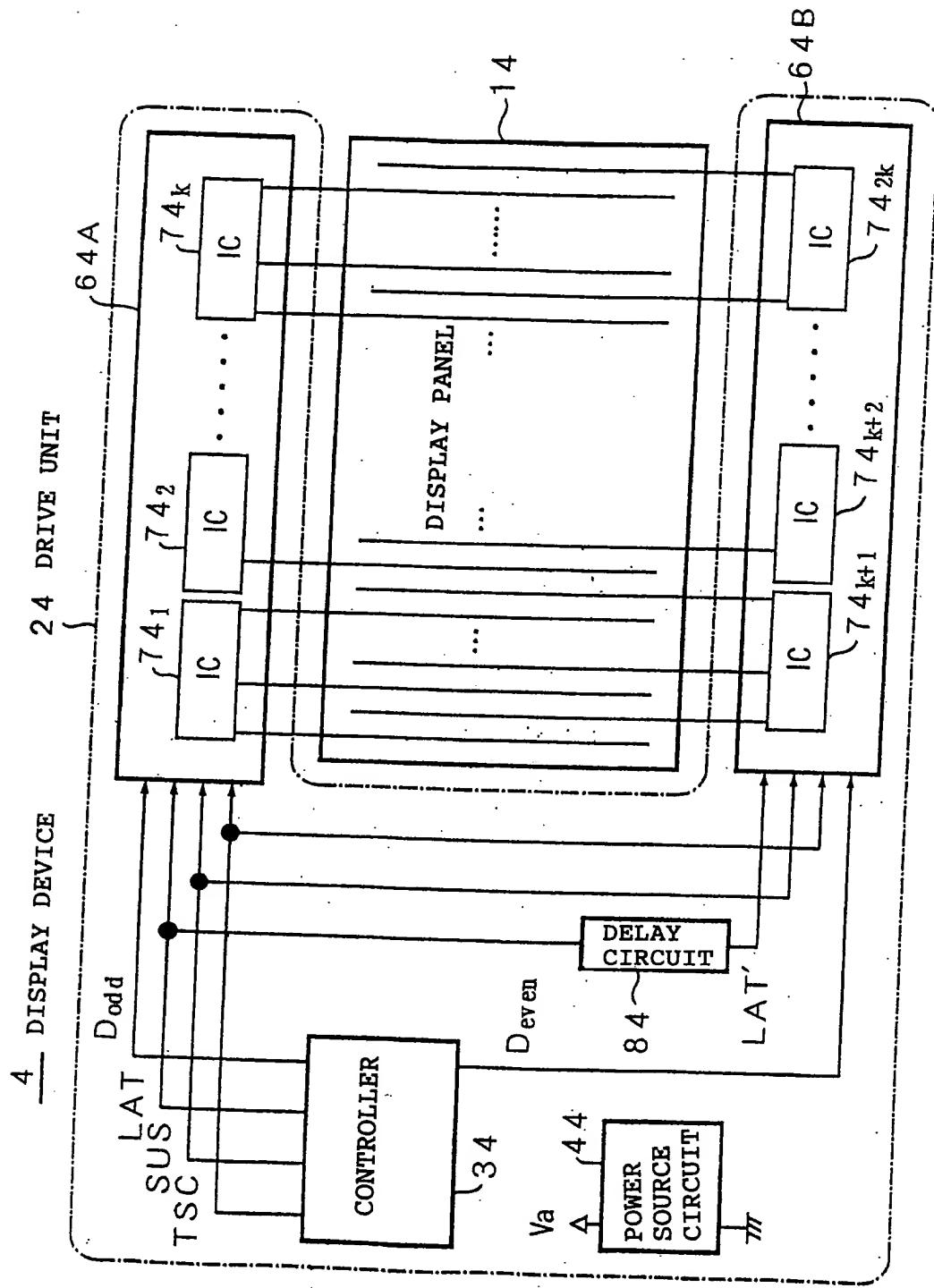


FIG. 15

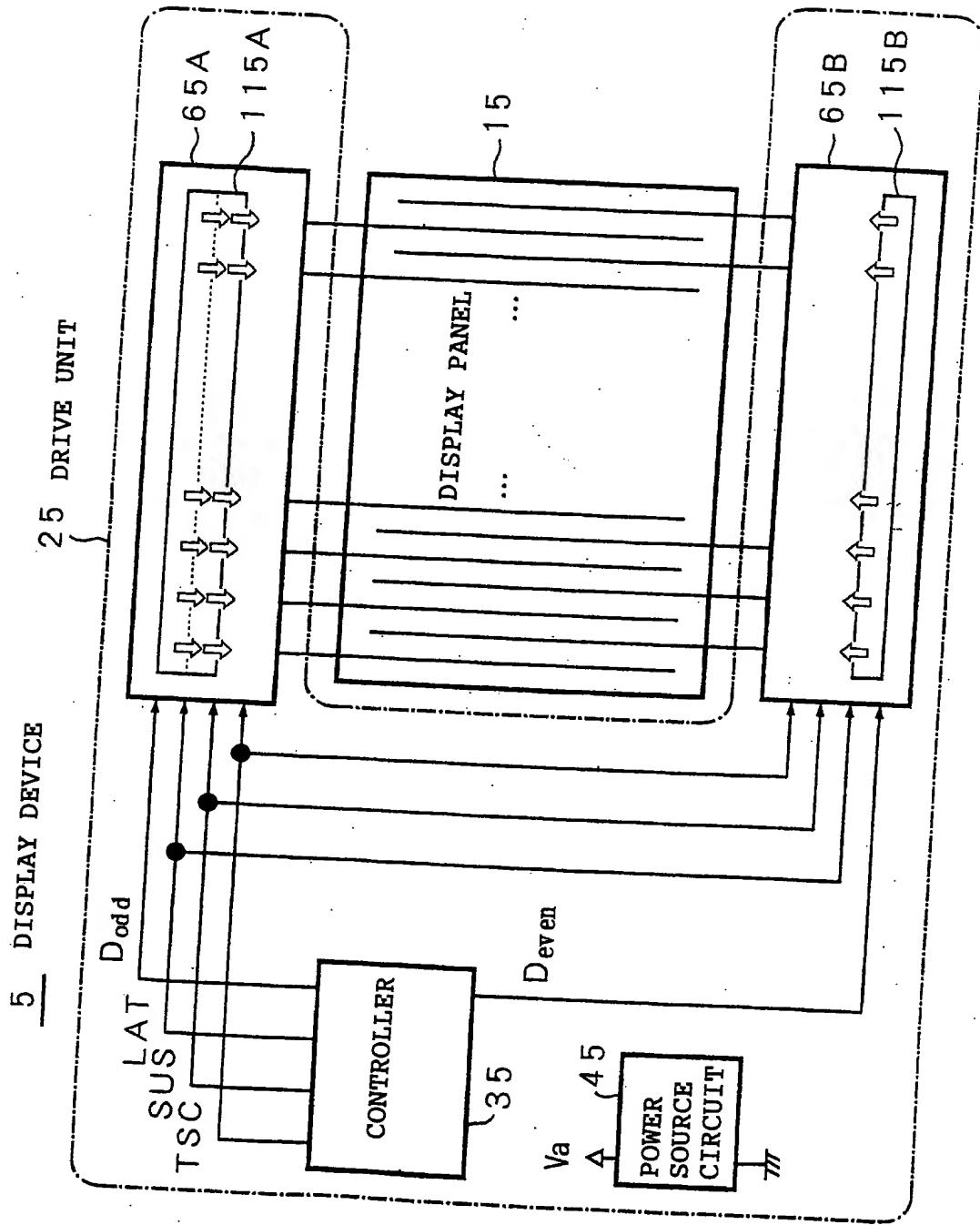


FIG. 16

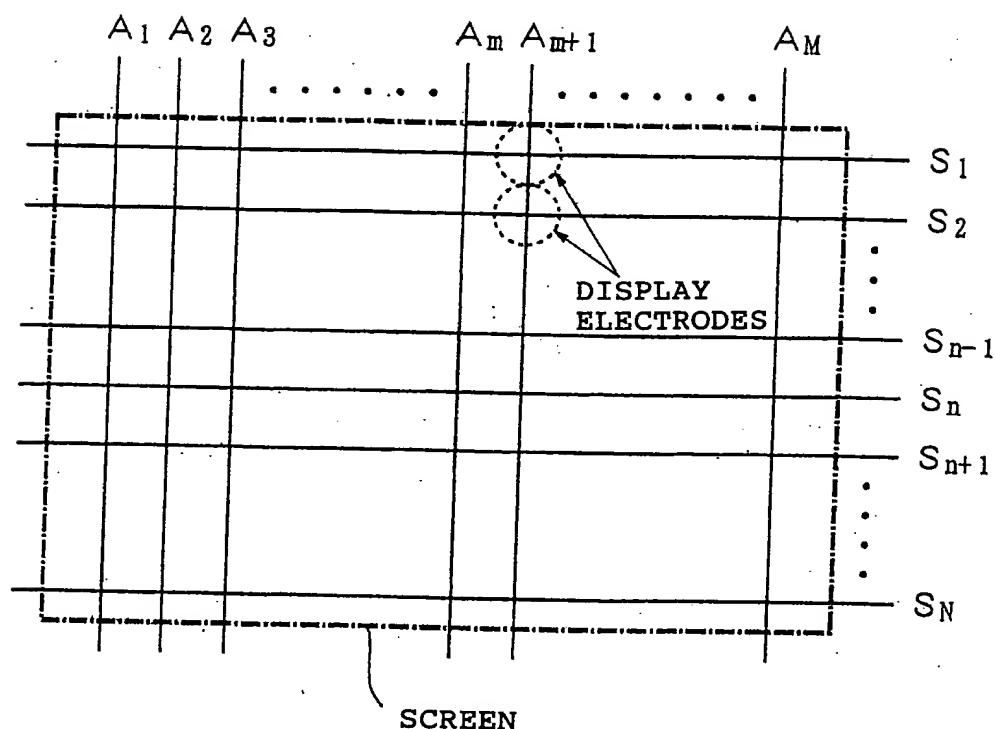


FIG. 17A

DISPLAY ELECTRODES
OF PDP

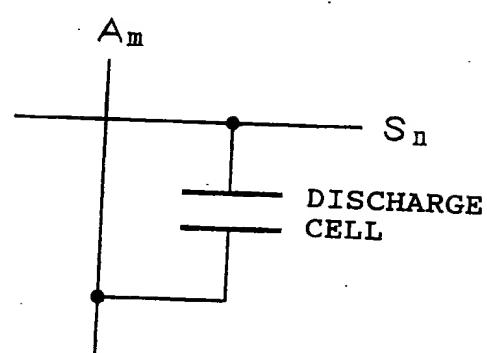


FIG. 17B

DISPLAY ELECTRODES
OF PALC

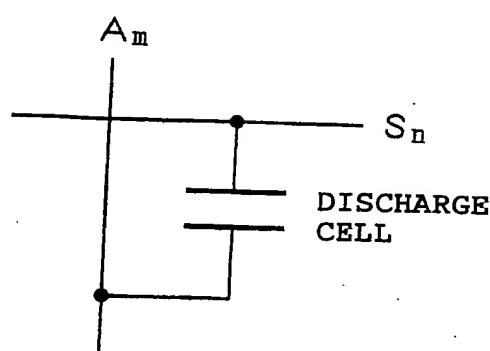


FIG. 17C

DISPLAY ELECTRODES
OF LCD

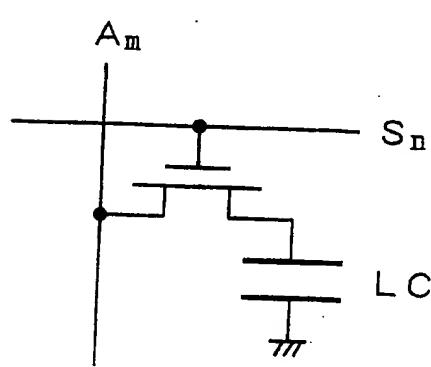


FIG. 17D

DISPLAY ELECTRODES
OF FED

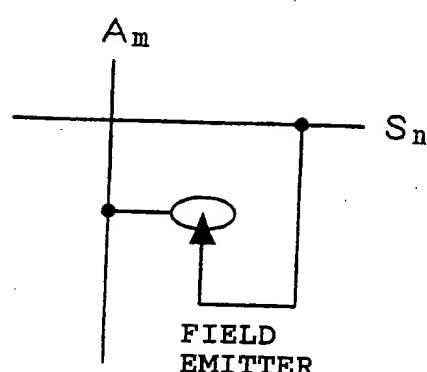
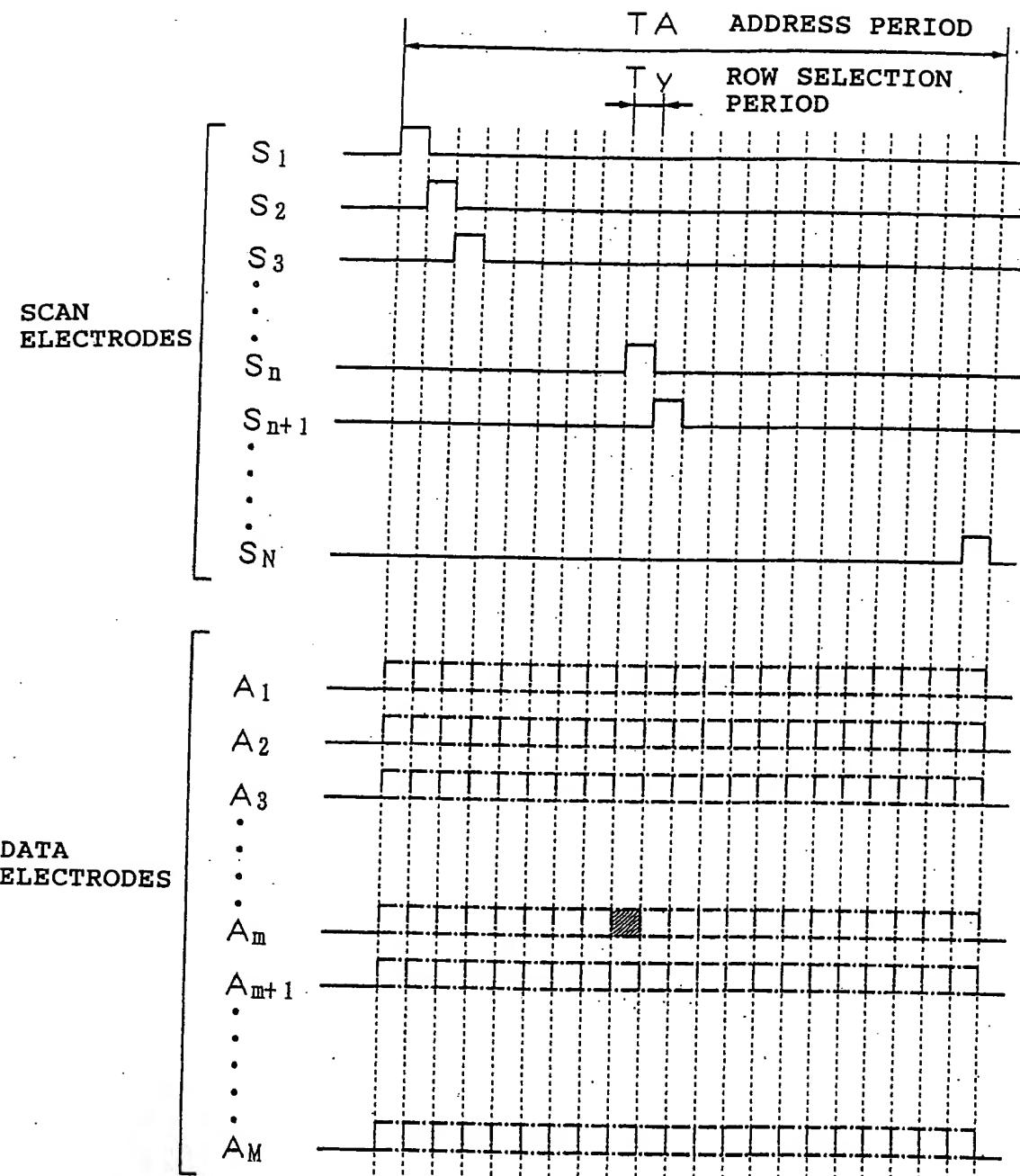


FIG. 18



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FIG. 19

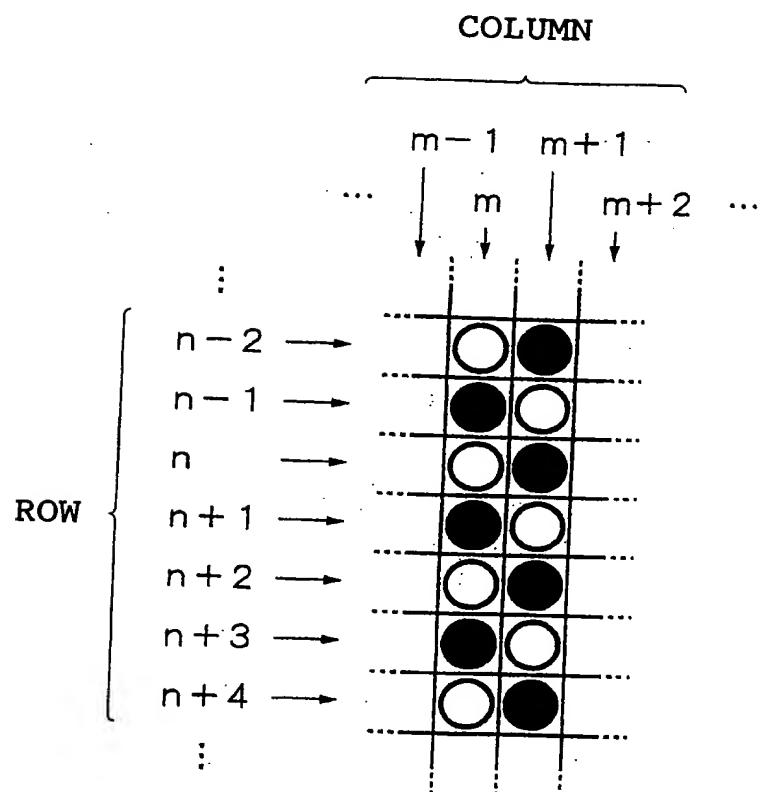


FIG. 20 Prior Art

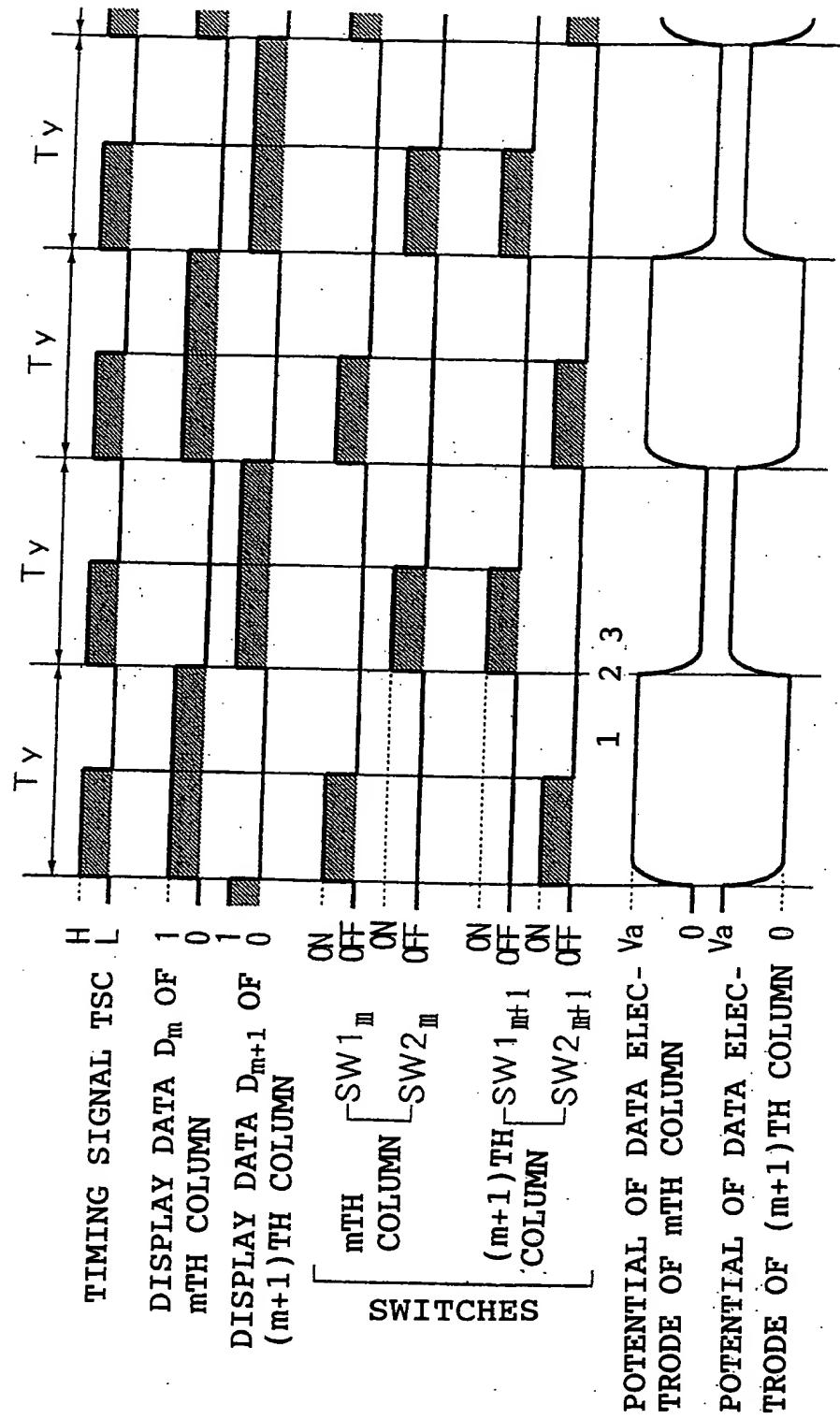


FIG. 21 Prior Art

